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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/760,131	01/15/2004	Kenneth Largman	60982-8006.US01 (A-70543-	7195

22918 7590 01/23/2007

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EXAMINER	
REVAK, CHRISTOPHER A	
ART UNIT	**PAPER NUMBER**
2131	

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	01/23/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary	Application No.	Applicant(s)	
	10/760,131	LARGMAN ET AL.	
	Examiner	Art Unit	
	Christopher A. Revak	2131	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *15 January 2004*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-20* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-20* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☒ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *15 January 2004* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date *12/4/06*.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____.

DETAILED ACTION

Information Disclosure Statement

1. The information disclosure statement (IDS) submitted on December 4, 2006 is in compliance with the provisions of 37 CFR 1.97. Accordingly, the examiner is considering the information disclosure statement.

Specification

2. The disclosure is objected to because of the following informalities: On page 2 of the specification, line 4, the referenced application number is missing. Additionally on page 2, the various listed co-pending cases have become US patents, and the status of the cases should be amended to indicate the patent numbers.

Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Golan, U.S. Patent 5,974,549.

As per claim 1, it is taught by Golan of an information appliance of the type having first storage for programs and data, and processor logic and executing computer program instructions to perform a task involving a user data; the information appliance characterized in that separate control processing environments and user processing environments are created and maintained so that user data having unknown or untrusted content is not exposed in the control processor logic environment to computer program code that can execute any computer program code instructions imbedded in the user data; and user data having unknown or untrusted content is only exposed in the user processor logic environment in a temporary storage different from the first storage when isolated from the first storage (col. 2, lines 13-28 & 39-57).

As per claim 2, it is disclosed by Golan of an information appliance comprising: at least one processing logic device for executing at least one instruction; a first storage for storing first data and first program code including said at least one instruction and including a user data; a second storage for storing second data; a switching system for selectably and independently coupling and decoupling the processing logic device with the first storage and/or the second storage under automated control, the switching system receiving at least one control signal from the processing logic device for selecting a condition of the switching system; the processing logic device operating in a control configuration and in a user data configuration according to the following conditions the processing logic device may be coupled with the first storage when the processing logic is loaded with a program instruction not capable of executing a data item that has untrusted content or that did not originate within a known controlled

environment; the processing logic device may not be coupled or only restrictively coupled to communicate known information with the first storage when the processing logic is loaded with a program instruction that may be capable of executing a data item that has untrusted content or that did not originate within a known controlled environment; the processing logic device may be coupled with the second storage when the processing logic is loaded with a program instruction that may be capable of executing a data item that has untrusted content or that did not originate within a known controlled environment; and the processing logic device may be coupled with the first storage and the second storage when the processing logic is loaded with a program instruction that is only capable of copying a data item from the first storage to the second storage or from the second storage to the first storage (col. 2, lines 13-28 & 39-57; col. 7, line 59 through col. 8, line 10; and as shown in Figure 1).

As per claim 3, Golan teaches wherein the switching system can couple or decouple the processing logic device with the first storage and the second storage in at least the following ways processing logic device coupled with the first storage only, processing logic device coupled with the second storage only, processing logic device coupled with the first and second storage concurrently, processing logic device coupled with neither the first storage nor the second storage (col. 1, lines 29-32 and col. 7, line 59 through col. 8, line 10).

As per claim 4, Golan discloses wherein the processing logic device comprises a microprocessor (col. 1, lines 29-32).

As per claim 5, it is taught by Golan wherein the processing logic device is selected from the set of processing logic circuits consisting of a microprocessor, a central processing unit (CPU), a controller, a micro-controller, an ASIC, a logic circuit, a programmable logic circuit, and combinations of these (col. 1, lines 29-32).

As per claim 6, it is disclosed by Golan wherein the information appliance is selected from the set of information appliances consisting of a computer, a notebook computer, a personal data assistant, a personal data organizer, a cellular telephone, a mobile telephone, a radio receiver, a radio transmitter, a GPS receiver, a satellite telephone, an automobile on-board computer, an aircraft on-board computer, a navigation device, a home appliance, a printing device, a scanning device, a camera, an electronic camera, a television receiver, a broadcast control system, an electronic instrument, a medical monitoring device, a security device, an environmental control system, a electronic device, and combinations of these (col. 1, lines 29-32).

As per claim 7, Golan teaches wherein the first data store and second data store are independently selectable and selected from the set of storage consisting of a rotating magnetic hard disk drive, a rotating magnetic floppy disk drive, a CD, a DVD, a semiconductor memory, a solid state memory, a chemical memory, a magnetic memory, a molecular memory, a micro-drive, a flash memory, a compact flash card memory, a RAM memory, a ROM memory, and combinations thereof (col. 7, line 59 through col. 8, line 10).

As per claim 8, Golan discloses wherein said at least one processing logic device comprises a plurality of processing logic devices (col. 1, lines 29-32).

As per claim 9, it is taught by Golan wherein at least one of said plurality of processing logic devices comprises at least one microprocessor and said at least one instruction comprises a plurality of computer program code segments from an operating system and a plurality of computer program code segments from an application program; and wherein said switching system is coupleable to said microprocessor for receiving switch control commands for altering the switch configuration to selectably couple and decouple the microprocessor with the first and second storage (col. 2, lines 13-28 & 39-57 and col. 7, line 59 through col. 8, line 10).

As per claim 10, it is disclosed by Golan wherein the plurality of processing logic devices are intermittently sequentially isolated and communicatively restricted, by an automated control system executing one of the processing logic devices (col. 1, lines 29-32).

As per claim 11, Golan teaches wherein the second storage is configured to perform as a temporary storage during a processing operation when it is coupled with the processing logic device and automatically erased after each processing has occurred independent if the processing completed with error condition or without error condition, where an error condition may include detection of a virus or other malicious code execution (col. 2, lines 13-28 & 39-57 and col. 7, line 59 through col. 8, line 10).

As per claim 12, Golan discloses wherein the plurality of processing logic devices and at least said first and second storage are dynamically configurable to create computing environments having determined characteristics (col. 7, line 59 through col. 8, line 10).

As per claim 13, it is taught by Golan wherein said first storage stores a master template file having operating system and application program components and a protected copy of user data (col. 5, lines 25-35).

As per claim 14, it is disclosed by Golan of a method for operating an information appliance of the type having at least one processing logic device for executing at least one instruction, a first storage for storing first data and first program code including said at least one instruction and including a user data, and a second storage for storing second data; the method comprising selectively and independently switching to couple and decouple the processing logic device with the first storage and/or the second storage under automated control upon receipt of at least one control signal from the processing logic device for selecting a condition of the switching system; operating the processing logic device in a control configuration and in a user data configuration according to the following conditions permitting coupling the processing logic device with the first storage when the processing logic is loaded with a program instruction not capable of executing a data item that has untrusted content or that did not originate within a known controlled environment; not permitting coupling the processing logic device with the first storage or only restrictively permitting coupling the processing logic device with the first storage to communicate known information when the processing logic is loaded with a program instruction that may be capable of executing a data item that has untrusted content or that did not originate within a known controlled environment; permitting coupling the processing logic device with the second storage when the processing logic is loaded with a program instruction that may be capable of

executing a data item that has untrusted content or that did not originate within a known controlled environment; and permitting coupling the processing logic device with the first storage and the second storage when the processing logic is loaded with a program instruction that is only capable of copying a data item from the first storage to the second storage or from the second storage to the first storage (col. 2, lines 13-28 & 39-57; col. 7, line 59 through col. 8, line 10; and as shown in Figure 1).

As per claim 15, Golan teaches of further comprising erasing the second storage after any processing logic device has used said second storage to process a user data (col. 7, line 59 through col. 8, line 10).

As per claim 16, Golan discloses wherein the information appliance is selected from the set of information appliances consisting of a computer, a notebook computer, a personal data assistant, a personal data organizer, a cellular telephone, a mobile telephone, a radio receiver, a radio transmitter, a GPS receiver, a satellite telephone, an automobile on-board computer, an aircraft on-board computer, a navigation device, a home appliance, a printing device, a scanning device, a camera, an electronic camera, a television receiver, a broadcast control system, an electronic instrument, a medical monitoring device, a security device, an environmental control system, a electronic device, and combinations of these (col. 1, lines 29-32).

As per claim 17, it is taught by Golan wherein said at least one processing logic device comprises a plurality of processing logic devices (col. 1, lines 29-32).

As per claim 18, it is disclosed by Golan wherein at least one of said plurality of processing logic devices comprises at least one microprocessor and said at least one

instruction comprises a plurality of computer program code segments from an operating system and a plurality of computer program code segments from an application program; and wherein said switching system is coupleable to said microprocessor for receiving switch control commands for altering the switch configuration to selectably couple and decouple the microprocessor with the first and second storage (col. 1, lines 29-32 and col. 7, line 59 through col. 8, line 10).

As per claim 19, Golan teaches of an information processing device comprising a housing having a form factor of a computer PC Card and a plurality of PCCardBus interface connections; a plurality of processors disposed within said housing; a plurality of data stores disposed within said housing or coupled thereto; a protected data store portion selected from said plurality of data stores for storing at least a user data; a data store switch system coupled with said plurality of data stores, said switch system coupled with a data store switch configuration for configuring communication with one or more data store disposed within said housing; an I/O switch system coupled with at least one peripheral, said I/O system coupled with an I/O system configuration including a plurality of traits for configuring communication with said peripheral disposed within said housing; a plurality of computing environments, each said computing environment including at least one processor and identified by at least one trait selected from said plurality of traits, including: a data store switch communication path coupled with said data store switch, said data store switch communication path coupling at least one data store with said computing environment according to said data store switch configuration; an I/O switch communication path coupled with said I/O switch system, said I/O switch

communication path for coupling said peripheral with said computing environment according to said I/O switch system configuration; said computing environment capable of performing a processing activity including receiving input from said I/O switch system and sending output to said I/O switch system, said processing activity performed independently of said processing activity of another computing environment; a control computing environment selected from said plurality of computing environments for configuring said data store switch configuration, for configuring said I/O switch system configuration, said data store switch configuration supporting communication between said control computing environment and said protected data store; and at least one user isolated computing environment selected from said plurality of computing environments; wherein said I/O switch system configuration is configured to direct a received input to at least one of said computing environment, said I/O switch system configuration is configured to direct an output generated by one or more of said plurality of computing environments to said peripheral (col. 2, lines 13-28 & 39-57; col. 7, line 59 through col. 8, line 10; and as shown in Figure 1).

As per claim 20, Golan discloses wherein the plurality of processors are independently selected from the set of processing logic circuits consisting of a microprocessor, a central processing unit (CPU), a controller, a micro-controller, an ASIC, a logic circuit, a programmable logic circuit, and combinations of these; and the plurality of data store are independently selectable and selected from the set of storage consisting of a rotating magnetic hard disk drive, a rotating magnetic floppy disk drive, a CD, a DVD, a semiconductor memory, a solid state memory, a chemical memory, a

magnetic memory, a molecular memory, a micro-drive, a flash memory, a compact flash card memory, a RAM memory, a ROM memory, and combinations thereof (col. 7, line 59 through col. 8, line 10).

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Christopher A. Revak whose telephone number is 571-272-3794. The examiner can normally be reached on Monday-Friday, 6:30am-3:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ayaz Sheikh can be reached on 571-272-3795. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

CR
January 21, 2007

CHRISTOPHER REVAK
PRIMARY EXAMINER